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CLAIMS

1- Method to control the access to a sector of a flash type memory of an electronic module which consists in checking that the owner of the data to be written has write access to a partition of said sector characterised in that it consists in checking at least one additional rule on the sector concerned in order to allow possible erasure of the entire said sector before writing in it.

2- Method according to claim 1, characterised in that the additional rule(s) are used to check that the write does not delete the data of an owner other than the one requiring the write access.

3- Method according to claim 2, characterised in that the check is based on a rule or a combination of rules of the following type: the write is authorised if:

- The entire sector belongs to the same owner, and/or
- The pages not belonging to the same owner are blank; and/or
- The pages not belonging to the same owner are marked as erasable, and/or

- The location in the sector where the write is to be made is blank, the sector not necessarily being completely blank.

4-Method according to one of claims 1 to 3, characterised in that it consists, if their owner so requests, in marking pages of a sector as erasable.

5-Electronic module including information processing means, a FLASH type non volatile memory characterised in that it includes a memory manager which consists in checking on the sector concerned at least one rule in addition to that which consists in checking that the owner of the data to be written has write access to a partition of said sector, in order to authorise possible erasure of the entire said sector before writing in it.

6-Module according to claim 5, characterised in that it intercepts all the

writes in Flash memory.

7-Module according to claim 5 or 6, characterised in that the memory manager 17 can access a description 27 of the memory sectors containing the
5 statuses of the pages of said sectors.

8-Card characterised in that it comprises the electronic module according to one of claims 5 to 7.

9-Computer program comprising program code instructions to execute the steps of the method according to one of claims 1 to 4 when said program is run in an electronic assembly.